Intel Research & Development

Graduate Technical Talk

Presented by Jeff Jones, Principle Engineer

Logic Technology Development Group

intel Look Inside.™

Raffle

Samsung Galaxy Tab® 3

Date: Wednesday, November 13
Time: 5:30 p.m.
Location: SGM 101 (Seeley G. Mudd Building)

We will be raffling an Intel based, Samsung Galaxy Tab 3 !!

Immediate Opportunity for PhD and MS Students

Interviews for qualified students to be held on Thursday, Nov 14th

- Electrical Engineering
- Mat Sci
- Optics
- Chemical Engineering
- Physics
- Chemistry

Dinner provided
Bring a resume

Join Jeff Jones as he discusses cutting edge developments and opportunities within Intel's Logic Technology Development Group. Jeff is a Principle Engineer with LTD's Advanced Design Group. Since joining Intel in 1996, Jeff has been involved in CPU design, silicon debug, and platform architecture - starting with Intel's 250 nm fab process and continuing to today's 10 nm process. His current areas of focus are Design/Process Pathfinding and Design Methodology Improvement.

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